

FIG. 1

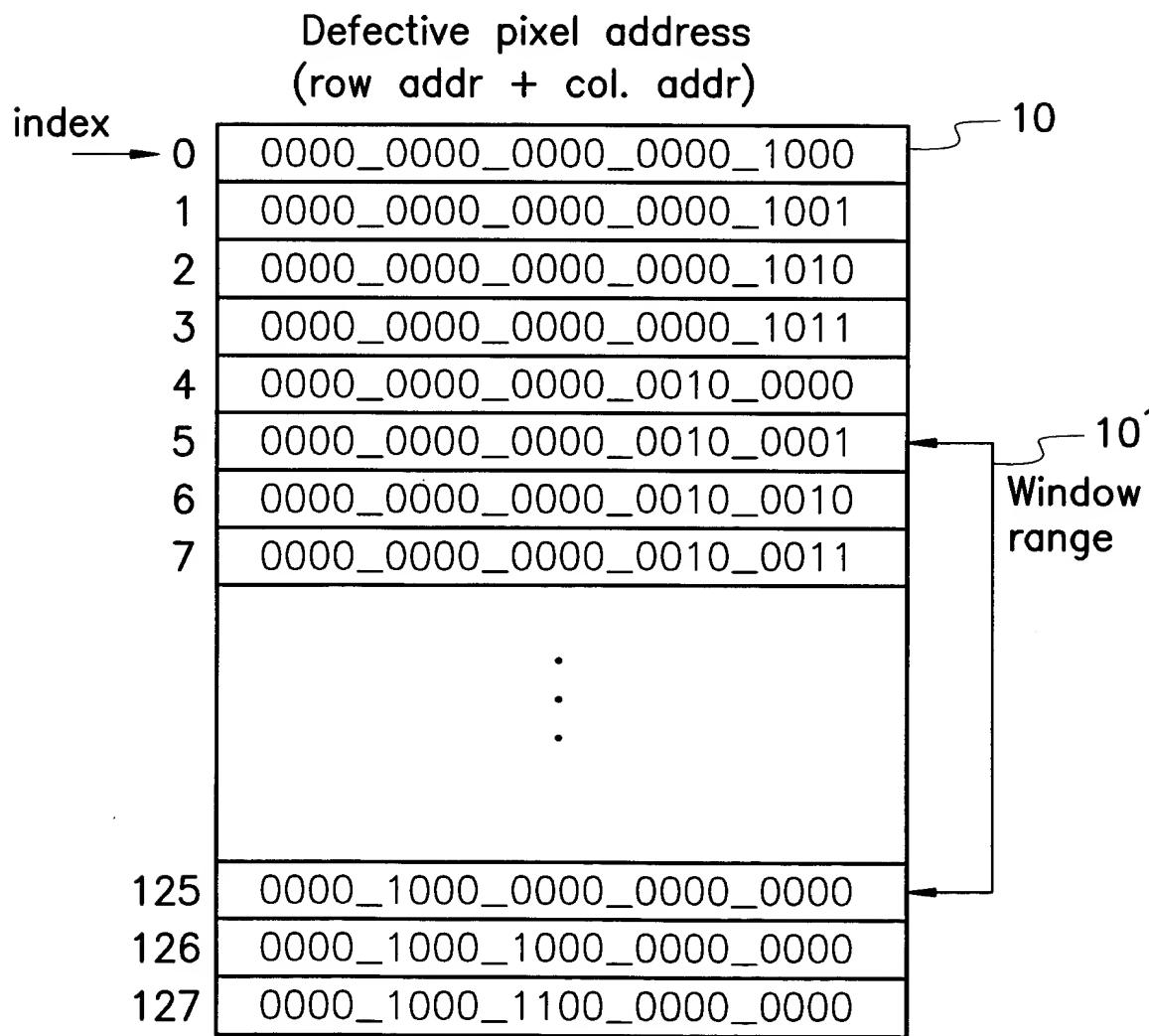


FIG. 2

Defective pixel address
(Row addr + Col. addr)

index →	0	0000_0000_0000_0000_0100
	1	0000_0000_0000_0000_0101
	2	0000_0000_0000_0000_0110
		⋮
	126	1111_1111_1111_1111_1111
	127	1111_1111_1111_1111_1111

FIG. 3

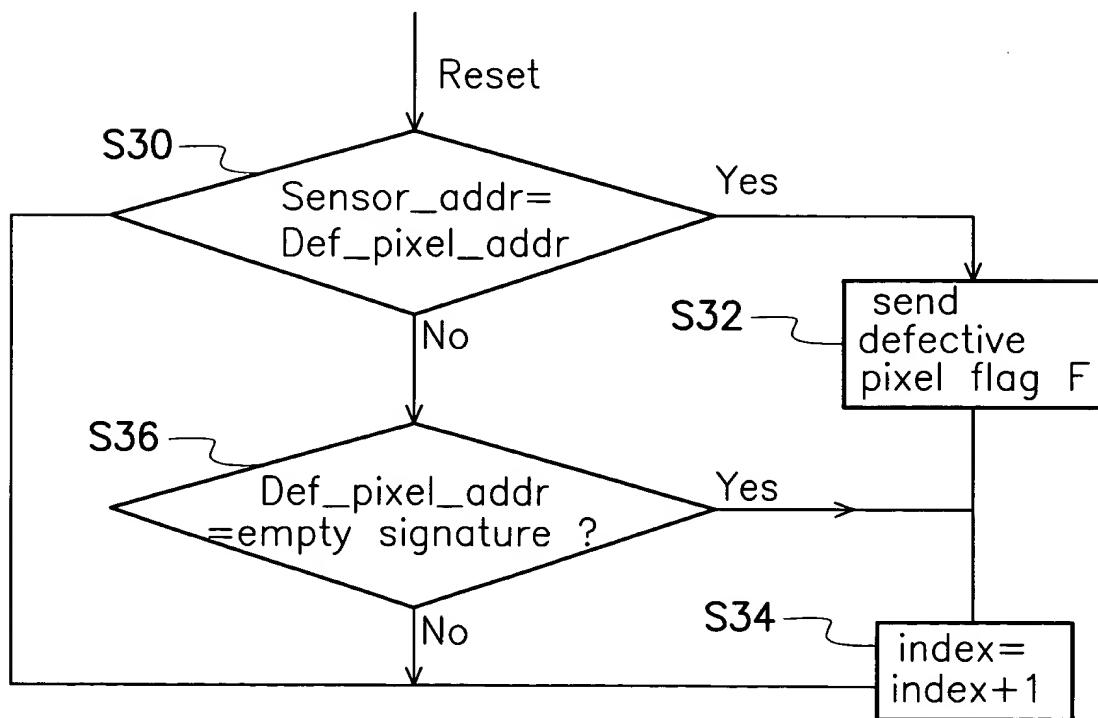


FIG. 4